

Appln No. 10/645,812

Amdt date March 2, 2005

Reply to Office action of December 2, 2004

**Amendments to the Specification:**

Please replace the paragraph beginning on page 8, line 17 through page 9, line 8 with the following rewritten paragraph:

In the FED structured as described above, predetermined external voltages are applied to the gate electrodes 6, the cathode electrodes 10, the metal mesh grid 22, and the anode electrode ~~16~~14. For example, a positive voltage of a few volts to a few tens of volts is applied to the gate electrodes 6, a negative voltage of a few volts to a few tens of volts is applied to the cathode electrodes 10, a positive voltage of a few volts to a few hundred volts is applied to the metal mesh grid 22, and a positive voltage of a few hundred to a few thousand volts is applied to the anode electrode 14. The cathode electrodes 10 would typically function as scanning electrodes while the gate electrodes 6 provide data information, or vice versa. As a result, electric fields are formed between the gate electrodes 6 and the cathode electrodes 10 (see equipotential lines 11 in FIG. 3A) such that electrons are emitted from the emitters 12. Also, the emitted electrons are formed into electron beams 13 (FIGs. 3A and 3B) and induced toward the phosphor layers 16 to land on the same, thereby illuminating the phosphor layers ~~22~~16 to realize predetermined images.